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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/039,035	01/04/2002	Nicholas P. Wilt	MSFT-0740/177740.01	2351
	7590 07/14/200 WASHBURN LLP (M	8 ICROSOFT CORPORATION)	EXAMINER	
CIRA CENTRE	CIRA CENTRE, 12TH FLOOR		DAO, THUY CHAN	
2929 ARCH STREET PHILADELPHIA, PA 19104-2891			ART UNIT	PAPER NUMBER
			2192	
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			07/14/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)
	10/039,035	WILT ET AL.
Office Action Summary	Examiner	Art Unit
	Thuy Dao	2192
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tir will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).
Status		
Responsive to communication(s) filed on <u>05 ∧</u> This action is FINAL . 2b) This Since this application is in condition for allowated closed in accordance with the practice under the process.	s action is non-final. ince except for formal matters, pro	
Disposition of Claims		
4) ☐ Claim(s) 1,3-9,11-18,20 and 22-26 is/are pend 4a) Of the above claim(s) 2,10,19 and 21 is/ar 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1,3-9,11-18,20 and 22-26 is/are rejection of the complete com	e withdrawn from consideration.	
9)☐ The specification is objected to by the Examine	er.	
10) ☐ The drawing(s) filed on 04 January 2002 is/are Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) ☐ The oath or declaration is objected to by the E	e: a) accepted or b) objected or b)	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority documen 2. ☐ Certified copies of the priority documen 3. ☐ Copies of the certified copies of the priority documen application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Applicationity documents have been received au (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D: 5) Notice of Informal F 6) Other:	

Art Unit: 2192

DETAILED ACTION

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114.

Applicant's submission filed on May 5, 2008 has been entered.

2. Claims 1, 3-9, 11-18, 20, and 22-26 have been examined.

Response to Amendments

- 3. In the instant amendments, claims 1, 4-5, 7, 9, 11, 14, 16, 18, 20, and 22-25 have been amended; claims 2, 10, 19, and 21 have been canceled.
- 4. The objection to claims 9 and 18 is withdrawn in view of Applicants' amendments.

Claim Objections

5. Claim 9 is objected to because of minor informality. The phrase in line 4 is considered to read as - -...said driver <u>program</u> interacting with a computing component...- - as recited in lines 3, 8, and 9.

Response to Arguments

6. Applicants' arguments have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections – 35 USC § 102

7. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

Art Unit: 2192

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

8. Claims 1, 3-9, 11-18, 20, and 22-26 are rejected under 35 U.S.C. 102(b) as being anticipated by "JavaOS for Business Version 2.0", Reference Manual, June 1998 (art made of record, hereafter "JavaOS").

Claim 1:

JavaOS discloses a computer system, comprising:

a processor (e.g., pages 1-6 and 1-7, FIG. 1-1, client computer with one or more processor);

an operating system (e.g., page 1-1, JavaOS; pages 1-5 and 1-6, FIG. 1-1, JavaOS in Client-Server environment)

having a driver comprising a plurality of instructions that interacts with a computing component, at least a portion of said driver instructions being in an intermediate language (e.g., page 1-2, JavaOS device drivers written in Java);

a plurality of application instructions separate from the driver instructions, said application instructions being in an intermediate language readable by an intermediate language compiler (e.g., page 1-12, Configuration Manager; page 1-13, Service Manager; page 2-18, free-formed package of Java classes; page 2-19, other JavaOS code);

a plurality of runtime instructions, said runtime instructions being in an intermediate language readable by an intermediate language compiler (e.g., page 1-9, JDK Runtime Layer),

wherein said runtime instructions performs the translation between said application instructions and said driver; (e.g., page 1-9 to page 1-13, JMV, JDK Hosting Classes, JavaOS Device Interface, JavaOS Platform Interface), and

an intermediate language compiler capable of compiling the application instructions the runtime instructions and said at least a portion of said driver instructions into a combined set of instructions executable by the processor for interacting with the

Art Unit: 2192

computing component (e.g., page 1-1, JavaOS as a combined/final set of instructions; pages 1-5 and 1-6, FIG. 1-1, JavaOS as a single executable program).

Claim 3:

The rejection of claim 1 is incorporated. JavaOS discloses the selected driver is split into user mode and kernel mode instructions (e.g., page 1-9 and FIG. 1-3).

Claim 4:

The rejection of claim 3 is incorporated. JavaOS discloses the user mode instructions of the driver translates from device driver interface instructions to hardware-specific commands (e.g., pages 1-13 and 1-16).

Claim 5:

The rejection of claim 4 is incorporated. JavaOS discloses the driver writes hardware-specific commands into an operating system-allocated buffer for submission to a scheduler of the hardware's time (e.g., pages 3-30 and 3-31).

Claim 6:

The rejection of claim 1 is incorporated. JavaOS discloses the plurality of application instructions and the plurality of runtime instructions are delivered to the computer system over a network (e.g., pages 1-7 and 1-8, FIG. 1-2, Client-Server network).

Claim 7:

The rejection of claim 1 is incorporated. JavaOS discloses *driver is delivered* over a network (e.g., pages 1-18 and 1-19, JavaOS System Database and Device Drivers).

Claim 8:

Art Unit: 2192

The rejection of claim 1 is incorporated. JavaOS discloses the intermediate language compiler comprises a Just-In-Time compiler (e.g., page 1-16).

Claim 9:

JavaOS discloses a method for software interaction with hardware, comprising:

receiving an application program in an intermediate programming language (e.g., page 1-12, Configuration Manager; page 1-13, Service Manager; page 2-18, free-formed package of Java classes; page 2-19, other JavaOS code);

receiving at least a portion of a driver program in an intermediate language separate from the application program instructions, said driver interacting with a computing component on a target computer system (e.g., page 1-2, JavaOS device drivers written in Java);

receiving runtime program in an intermediate programming language (e.g., page 1-9, JDK Runtime Layer),

wherein said runtime program performs the translation between said application instructions and said driver program (e.g., page 1-9 to page 1-13, JMV, JDK Hosting Classes, JavaOS Device Interface, JavaOS Platform Interface);

compiling the application program and the runtime program and the driver program into a single executable program for execution on the target computer system (e.g., page 1-1, JavaOS as a combined/final set of instructions; pages 1-5 and 1-6, FIG. 1-1, JavaOS as a single executable program).

Claim 11:

The rejection of claim 9 is incorporated. JavaOS discloses the driver program comprises a kernel mode portion in an executable form (e.g., pages 1-6 and 1-7, FIG. 1-1).

Claim 12:

Application/Control Number: 10/039,035

Art Unit: 2192

The rejection of claim 11 is incorporated. JavaOS discloses the driver program comprises a user mode portion provided in the intermediate language form (e.g., page

Page 6

1-2).

Claim 13:

The rejection of claim 12 is incorporated. JavaOS discloses the user mode portion translates from device driver interface instructions to hardware-specific

commands (e.g., pages 1-13 and 1-16).

Claim 14:

The rejection of claim 9 is incorporated. JavaOS discloses the driver program

writes hardware-specific commands into an operating system-allocated buffer for

submission to a scheduler of the hardware's time (e.g., pages 3-30 and 3-31).

Claim 15:

The rejection of claim 9 is incorporated. JavaOS discloses the application

program and the runtime program are delivered to the target computer system over a

network (e.g., pages 1-18 and 1-19).

Claim 16:

The rejection of claim 9 is incorporated. JavaOS discloses the driver program is

delivered over a network (e.g., pages 1-7 and 1-8).

Claim 17:

The rejection of claim 9 is incorporated. JavaOS discloses the step of compiling

uses a Just-In-Time compiler (e.g., page 1-16).

Claim 18:

JavaOS discloses a computer-readable medium having stored thereon

computer-executable instructions for software interaction with hardware, comprising:

Art Unit: 2192

instructions for receiving an application program in an intermediate programming language (e.g., page 1-12, Configuration Manager; page 1-13, Service Manager; page 2-18, free-formed package of Java classes; page 2-19, other JavaOS code);

instruction for receiving at least a portion of a driver program in an intermediate language separate from the application program instructions, said driver program interacting with a computing component on a target computer system (e.g., page 1-2, JavaOS device drivers written in Java); and

instructions for receiving a runtime program in an intermediate programming language (e.g., page 1-9, JDK Runtime Layer),

wherein said runtime program performs the translation between said application instructions and said driver program (e.g., page 1-9 to page 1-13, JMV, JDK Hosting Classes, JavaOS Device Interface, JavaOS Platform Interface);

instructions for compiling the application program, the runtime program and the driver program into a single executable program for execution on the target computer system (e.g., page 1-1, JavaOS as a combined/final set of instructions; pages 1-5 and 1-6, FIG. 1-1, JavaOS as a single executable program).

Claim 20:

The rejection of claim 18 is incorporated. JavaOS discloses the driver program comprises a kernel mode portion provided in an executable form wherein the at least a portion of the driver program in an intermediate language received comprise user mode instructions (e.g., pages 1-18 and 1-19, Device Drivers and JavaOS System Database).

Claim 22:

The rejection of claim 20 is incorporated. JavaOS discloses the user mode instructions translate from device driver interface instructions to hardware-specific commands (e.g., pages 1-13 and 1-16).

Claim 23:

Art Unit: 2192

The rejection of claim 22 is incorporated. JavaOS discloses the driver program writes hardware-specific commands into an operating system-allocated buffer for submission to a scheduler of the hardware's time (e.g., pages 3-30 and 3-31).

Claim 24:

The rejection of claim 18 is incorporated. JavaOS discloses *receiving the application program and the runtime program over a network* (e.g., pages 1-17 and 1-8).

Claim 25:

The rejection of claim 18 is incorporated. JavaOS discloses *receiving the driver* program over a network (e.g., pages 1-7 and 1-8).

Claim 26:

The rejection of claim 18 is incorporated. JavaOS discloses the step of compiling uses a Just-In-Time compiler (e.g., page 1-16).

Conclusion

9. Any inquiry concerning this communication should be directed to examiner Thuy Dao (Twee), whose telephone/fax numbers are (571) 272 8570 and (571) 273 8570, respectively. The examiner can normally be reached on every Tuesday, Thursday, and Friday from 6:00AM to 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam, can be reached at (571) 272 3695.

The fax phone number for the organization where this application or proceeding is assigned is (571) 273 8300.

Any inquiry of a general nature of relating to the status of this application or proceeding should be directed to the TC 2100 Group receptionist whose telephone number is (571) 272 2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for

Art Unit: 2192

published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Thuy Dao/

Examiner, Art Unit 2192

/Tuan Q. Dam/

Supervisory Patent Examiner, Art Unit 2192